

RECEIVED  
CENTRAL FAX CENTER

JUN 08 2007

REMARKS

Claims 1-15 and 22-26 are pending in the present application. Claims 16-21 were previously canceled. Claims 1 and 7 have been amended. No new matter has been added. Applicant respectfully requests reconsideration of the claims in view of the following remarks.

Applicant wishes to thank the Examiner for allowing claims 22-26.

Claims 1-15 have been rejected under 35 U.S.C. § 103(a) as being unpatentable over Katsuhisa (U.S. Patent No. 6,278,652, hereinafter "Katsuhisa"), in view of Benno et al. (U.S. Patent Application Publication No. 2003/0226054, hereinafter "Benno"). Applicant respectfully traverses this rejection.

Claim 1, as amended, recites an enabling circuit that "disables the clock receiver for a first time period after being triggered by a transition on the internal clock signal." Neither Katsuhisa nor Benno teach or suggest an enabling circuit that disables the clock receiver after being triggered by a transition on the internal clock signal. Benno, in Figure 3, clearly shows that extension signal S21, which disables the clock receiver, is triggered asynchronously by detected external noise on S13 at  $T_{noise}$ , *not* by transitions on the internal clock S12 at  $T_1$ ,  $T_2$ ,  $T_3$ ,  $T_n$ , or  $T_{n+1}$ . Benno, Figure 3. Whether or not the FREQUENCY DIVISION CIRCUIT 201 shown in Benno Figure 2 is disabled is completely independent of the state of internal clock S12 and entirely dependent on the state of S21. S12 can either be HIGH or LOW and FREQUENCY DIVISION CIRCUIT 201 will still function. Only when S21 is high will the FREQUENCY DIVISION CIRCUIT 201 cease to provide a dynamic clock signal on S12. Applicant, therefore, respectfully submits that claim 1 is not anticipated by the prior art of record.

Claims 2-6 depend from claim 1 and add further limitations. It is respectfully submitted that these dependent claims are allowable by reason of depending from an allowable claim as well as for adding new limitations.

Claim 7, as amended, recites a "pulse generator generating a pulse signal for a first time period upon being triggered by a transition in the internal clock signal." Neither Katsuhisa nor Benno teach or suggest a pulse generator triggered by a transition in the internal clock signal. To the contrary, HOLDING CIRCUIT 203 shown in Benno Figure 2, which the Office Action equates as the pulse generator, is clearly triggered by NOISE DETECTION CIRCUIT 104, *not* by internal clock S12. Benno, Figure 2, paragraph [0077]; Office Action, page 5. Applicant, therefore, respectfully submits that claim 7 is not anticipated by the prior art of record.

Claims 8-15 depend from claim 7 and add further limitations. It is respectfully submitted that these dependent claims are allowable by reason of depending from an allowable claim as well as for adding new limitations.

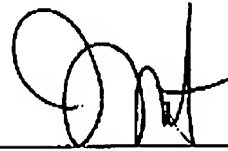
o

Applicant has made a diligent effort to place the claims in condition for allowance. However, should there remain unresolved issues that require adverse action, it is respectfully requested that the Examiner telephone Ira S. Matsil, Applicant's attorney, at 972-732-1001 so that such issues may be resolved as expeditiously as possible. A fee sheet is provided herewith. In the event that there are any additional fees due, please charge the same, or credit any overpayment, to Deposit Account No. 50-1065.

Respectfully submitted,

6/8/07

Date



Ira S. Matsil  
Attorney for Applicant  
Reg. No. 35,272

SLATER & MATSIL, L.L.P.  
17950 Preston Rd., Suite 1000  
Dallas, Texas 75252  
Tel.: 972-732-1001  
Fax: 972-732-9218